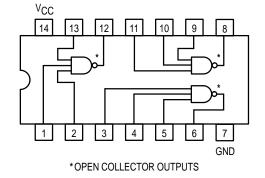


TRIPLE 3-INPUT NAND GATE

SN54/74LS12

TRIPLE 3-INPUT NAND GATE LOW POWER SCHOTTKY





J SUFFIX CERAMIC CASE 632-08



N SUFFIX PLASTIC CASE 646-06



D SUFFIX SOIC CASE 751A-02

ORDERING INFORMATION

SN54LSXXJ SN74LSXXN SN74LSXXD Ceramic Plastic SOIC

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
TA	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
Voн	Output Voltage — High	54, 74			5.5	V
lOL	Output Current — Low	54 74			4.0 8.0	mA

SN54/74LS12

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Test Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
\/	Input LOW Voltage	54			0.7	V	Guaranteed Input LOW Voltage for All Inputs	
VIL		74			0.8			
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	V _{CC} = MIN, I _{IN} = -18 mA	
IOH	Output HIGH Current	54, 74			100	μΑ	V _{CC} = MIN, V _{OH} = MAX	
V _{OL}	Output LOW Voltage	54, 74		0.25	0.4	V	I _{OL} = 4.0 mA	V _{CC} = V _{CC} MIN, V _{IN} = V _{IL} or V _{IH} per Truth Table
		74		0.35	0.5	V	I _{OL} = 8.0 mA	
I	Input HIGH Current				20	μΑ	$V_{CC} = MAX$, $V_{IN} = 2.7 V$	
ΊΗ					0.1	mA	$V_{CC} = MAX$, $V_{IN} = 7.0 V$	
IIL	Input LOW Current				-0.4	mA	$V_{CC} = MAX$, $V_{IN} = 0.4 V$	
Icc	Power Supply Current Total, Output HIGH Total, Output LOW				1.4	mA	V _{CC} = MAX	
					3.3			

AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

		Limits		Limits		Limits		
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions		
tPLH	Turn-Off Delay, Input to Output		17	32	ns	V _{CC} = 5.0 V		
tPHL	Turn-On Delay, Input to Output		15	28	ns	C_L = 15 pF, R_L = 2.0 k Ω		